

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L9	4001	(CDR\$1 or clock adj1 data adj1 recover\$3 or clock adj2 data adj2 circuit\$3 or data adj2 clock\$3 adj2 recover\$3 or clock adj1 data or DCR\$1) same (clock adj2 source or clock adj2 generat\$4 or clock near1 ref or clock near2 reference or clock adj2 distribution or clock adj2 process\$4 or clock adj2 circuit\$3 or refclk\$1 or CLKREF)	US-PGPUB ; USPAT ; EPO; JPO	2006/11/08 12:15	
2	BRS	L10	1540	(CDR\$1 or clock adj1 data adj1 recover\$3 or clock adj2 data adj2 circuit\$3 or data adj2 clock\$3 adj2 recover\$3 or clock adj1 data) with (PLL\$1 or DLL\$1 or PPLL\$1 or PDLL\$1 or phase adj1 lock\$3 adj1 loop\$1 or delay\$3 adj2 lock\$3 adj2 loop\$1 or DDLL\$1 or DPLL\$1)	US-PGPUB ; USPAT ; EPO; JPO	2006/11/08 11:58	

	Error Definition	Err ors
1		
2		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
3	BRS	L11	8946	(PLL\$1 or DLL\$1 or PPLL\$1 or PDLL\$1 or phase adj1 lock\$3 adj1 loop\$1 or delay\$3 adj2 lock\$3 adj2 loop\$1 or DDLL\$1 or DPLL\$1) near3 (controller\$1 or monitor\$3 or measur\$5 near3 control\$4 or control\$4 adj2 circuit\$3 or control\$4 adj2 device or timing near2 control\$4 or control\$4 adj1 signal\$1 or control\$4 near2 device or control\$4 near2 generat\$4 or control\$4 near2 process\$4 or timing adj2 device or timing adj2 circuit\$3)	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 11:10	
4	BRS	L12	538	(CDR\$1 or DCR\$1 or data adj1 clock\$ near2 recover\$3 or clock adj2 data adj2 recover\$3) near3 (controller\$1 or monitor\$3 or measur\$5 near3 control\$4 or control\$4 adj2 circuit\$3 or control\$4 adj2 device or timing near2 control\$4 or control\$4 adj1 signal\$1 or control\$4 near2 device or control\$4 near2 generat\$4 or control\$4 near2 process\$4 or timing adj2 device or timing adj2 circuit\$3)	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 11:13	

	Error Definition	Err ors
3		
4		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L13	64	11 and 12	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 11:13	
6	BRS	L14	806	9 and 10	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 11:25	
7	BRS	L15	558	9 same 10	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 11:25	
8	BRS	L16	153	15 and "375"/\$.ccls.	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 12:14	
9	BRS	L17	150	16 not 13	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 11:31	

	Error Definition	Err ors
5		
6		
7		
8		
9		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
10	BRS	L18	331	(data adj1 recover\$3 or data adj1 detect\$4) with (PLL\$1 or DLL\$1 or PPLL\$1 or PDLL\$1 or phase adj1 lock\$3 adj1 loop\$1 or delay\$3 adj2 lock\$3 adj2 loop\$1 or DDL\$1 or DPLL\$1) same (controller\$1 or monitor\$3 or measur\$5 near3 control\$4 or control\$4 adj2 circuit\$3 or control\$4 adj2 device or timing near2 control\$4 or control\$4 adj1 signal\$1 or control\$4 near2 device or control\$4 near2 generat\$4 or control\$4 near2 process\$4 or timing adj2 device or timing adj2 circuit\$3 or control\$4 near2 logic or programmabl\$1 near2 control\$4 or programmable near2 logic)	US- PGPUB ; USPAT ; EPO; JPO	2006/11/0 8 12:05	
11	BRS	L35	109	18 and "375"/\$.ccls.	US- PGPUB ; USPAT ; EPO; JPO	2006/11/0 8 12:16	
12	BRS	L36	137	(CDR\$1 or clock adj1 data adj1 recover\$3 or clock adj2 data adj2 circuit\$3 or data adj2 clock\$3 adj2 recover\$3 or clock adj1 data or DCR\$1) same 18	US- PGPUB ; USPAT ; EPO; JPO	2006/11/0 8 12:15	

	Error Definition	Err ors
10		
11		
12		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
13	BRS	L37	44	36 and "375"/\$.ccls.	US- PGPUB ; USPAT ; EPO; JPO	2006/11/08 12:16	

	Error Definition	Err ors
13		